

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: KAJIHARA, et al.  
Serial No.: 09/989,242  
Filed: November 21, 2001  
For: LEADFRAME SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE USING THE SAME, AND METHOD  
OF AND PROCESS FOR FABRICATING THE TWO  
Group: 2815  
Examiner: J. Clark  
Conf. No.: 9603

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR 1.97 & 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 9, 2008

Sir:

In the matter of the above-identified application, applicant(s) are submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and a copy of the document listed on the attached Form PTO/SB/08A for the Examiner's consideration.

This information disclosure statement is being submitted before the mailing date of a notice of allowance and is accompanied by the fee of \$180.00 set forth in 37 CFR 1.17(p) and by the following certification specified in 37 CFR 1.97(e).

On information and belief, I hereby certify that each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

To the extent the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance is satisfied by an English abstract.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case No. 1374.32049RV2), and please credit any excess fees to such deposit account.

Respectfully submitted,

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